RYUKA.009AUS PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kentaro Fukushima et al. ) Group Art Unit

Serial No. : New Application

Filed : Concurrently Herewith

For : A CONNECTION UNIT, A BOARD

FOR MOUNTING A DEVICE UNDER TEST, A PROBE CARD AND A DEVICE INTERFACING

PART

Examiner : Unknown

## INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks Washington, D.C. 20231

## Dear Sir:

Enclosed is a form PTO-1449 listing four prior art references relevant to the above-identified application. A copy of the four references and three English abstracts is also enclosed herewith.

Three Japanese patent publications are cited in the International Search Report issued by Japanese Patent Office in the international stage of this case. A copy of the International Search Report is also attached herewith. Since all of the prior art references are non-English documents, an English abstract is attached to each of the references. The Japanese article "Build-Up Multilayer Printed wiring Board Technology" is mentioned in the specification of the instant application at paragraph [0009]. The prior art references enclosed are:

Serial No.

: New Application

Filed

Concurrently Herewith

(1) Japanese Patent Laid-Open Publication No. 64-053429 is relevant to the present invention because it discloses a device for testing semiconductor chips with a structure which separately assembling the probe groups.

(2) Japanese Patent Laid-Open Publication No. 2000-081461 is relevant to the present invention because it discloses a structure of a semiconductor mounting device and connector for testing a semiconductor device.

(3) Japanese Patent Laid-Open Publication No. 11-064442 is relevant to the present invention because it discloses a structure of a jig for electric inspection by a flip chip single body.

Applicant respectfully requests that the prior art references submitted be considered in the substantive examination of this application.

Respectfully submitted,
MURAMATSU & ASSOCIATES

Dated.

8/12/04

By:

Masuo Muramatsu

Registration No. 38,684

Attorney of Record

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	U.S. PATENT DOCUMENTS								
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<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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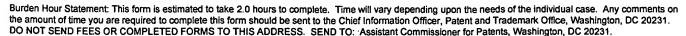
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